

**IN THE UNITED STATES PATENT OFFICE**

In Re Patent of

First Named Inventor: Park, Kee

Patent No.: 6,870,749

Issue Date: 3/22/2005

For: CONTENT ADDRESSABLE MEMORY  
(CAM) DEVICES WITH DUAL-  
FUNCTION CHECK BIT CELLS THAT  
SUPPORT COLUMN REDUNDANCY  
AND CHECK BIT CELLS WITH  
REDUCED SUSCEPTIBILITY TO SOFT  
ERRORS

Application No.: 10/619,635

Confirmation No.: 1230

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Statement Under 37 CFR §3.73(b)**

Sir:

NetLogic Microsystems, Inc., a Delaware corporation, hereby states that it is the assignee of the entire right, title and interest in the above-identified patent by virtue of the Assignments that are recorded in the USPTO at Reel/Frame **014306/0255** and **022980/0624**.

The undersigned is authorized to act on behalf of the assignee at least for purposes of making the foregoing statement.

Effective immediately, please direct all further communications in the above-identified patent application to the following address:

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